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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Docket No.:

BUR920040050US1

Kiran V. Chatty et al.

Serial No.: 10/711,748

Group Art Unit: 2819

Filed: 10/01/2004

Examiner: unknown

For: **HIGH VOLTAGE ESD POWER CLAMP**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. A copy of the cited reference is enclosed for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

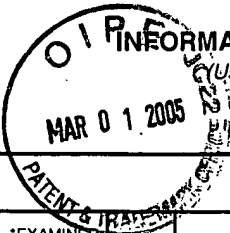
Please charge any deficiencies in fees and credit any overpayment of fees to International Business Machines Corporation's Deposit Account No. 09-0456.

Respectfully submitted,

William D. Sabo

Registration No. 27,465

International Business Machines Corporation
Intellectual Property Law - Zip 972E
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				ATTY DOCKET NO. BUR920040050US1		APPLICATION NO. 10/711,748		
				APPLICANT(S) Kiran V. Chatty et al.				
				FILING DATE 10/01/2004		GROUP ART UNIT 2819		
U.S. PATENT DOCUMENTS								
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
		6,657,835	12/02/2003	Ker et al.				
		6,433,979	08/13/2002	Yu				
		6,140,682	10/31/2000	Liu et al.				
		5,932,918	08/03/1999	Krakauer				
		5,596,474	01/21/1997	Wada et al.				
U.S. PATENT APPLICATION PUBLICATIONS								
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
		Steven H. Voldman, "The State of the Art of Electrostatic Discharge Protection: Physics, Technology, Circuits, Design, Simulation, and Scaling", IEEE Journal of Solid-State Circuits, Vol. 34, No. 9, September 1999, pages 1272-1282						
EXAMINER					DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				ATTY DOCKET NO. BUR920040050US1		APPLICATION NO. 10/711,748	
				Kiran V. Chatty et al.			
				FILING 10/01/2004		GROUP ART 2819	

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,345,357	09/06/1994	Pianka			
		5,345,356	09/06/1994	Pianka			
		5,239,440	08/24/1993	Merrill			

U.S. PATENT APPLICATION PUBLICATIONS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>			

EXAMINER	DATE CONSIDERED
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